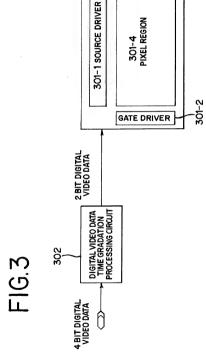


301-3

GATE DRIVER



301

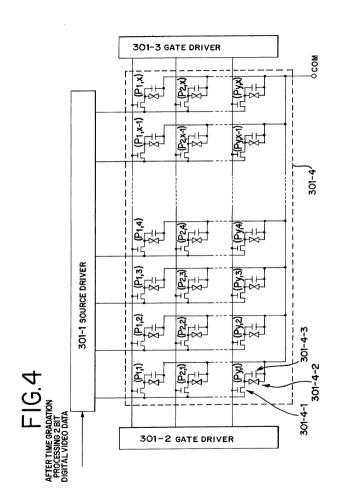
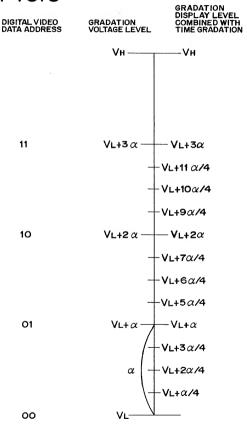
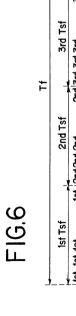
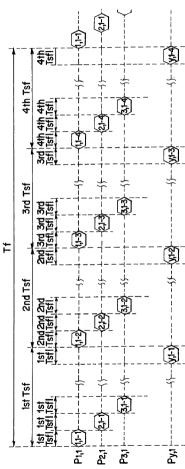


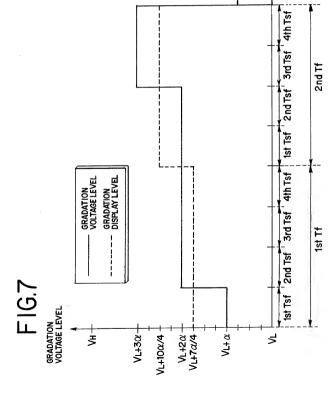
FIG.5

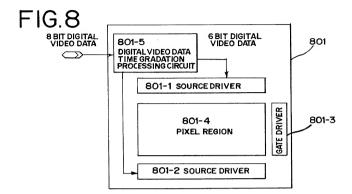






TIME





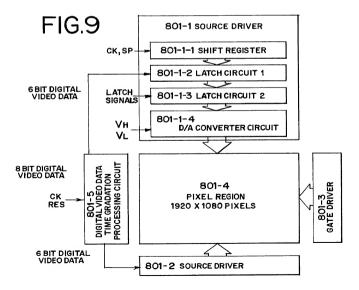
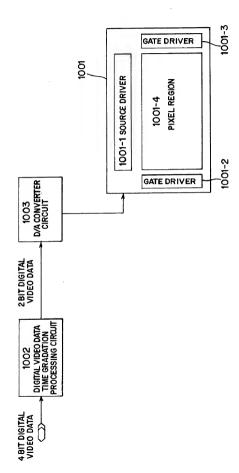
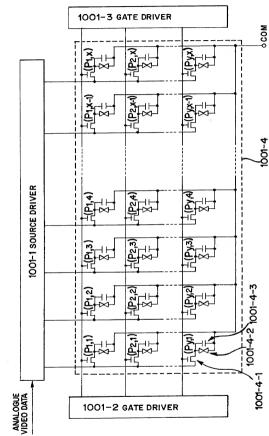


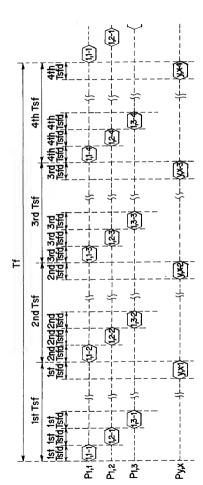
FIG.10







F1G.12



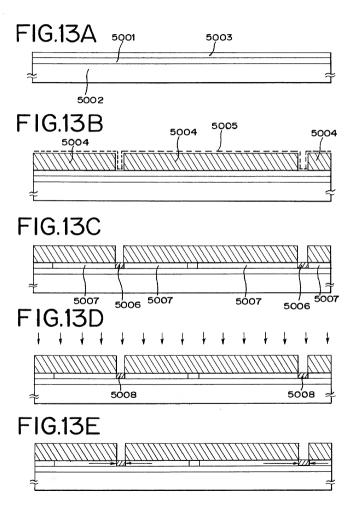


FIG.14A

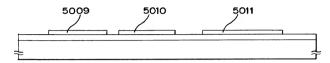


FIG.14B

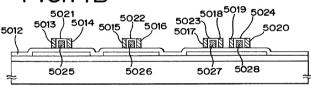
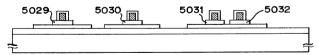


FIG.14C



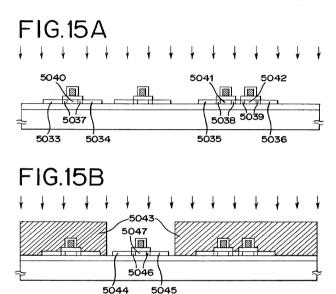


FIG.15C

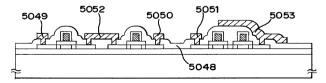


FIG.16A

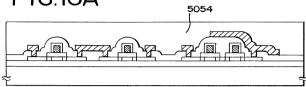


FIG.16B

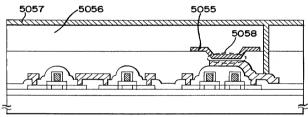
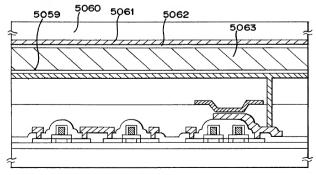
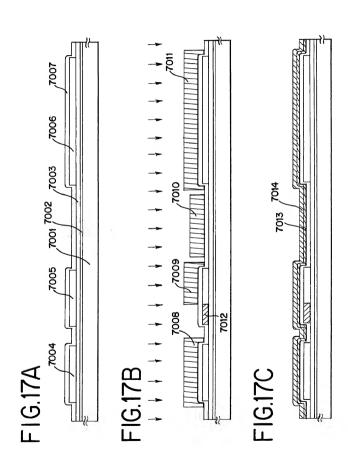
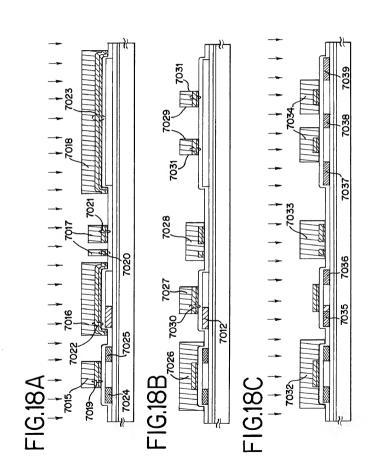
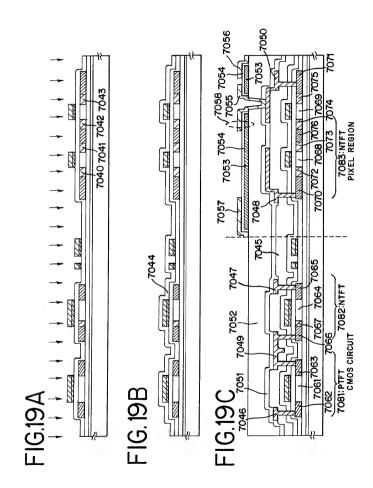


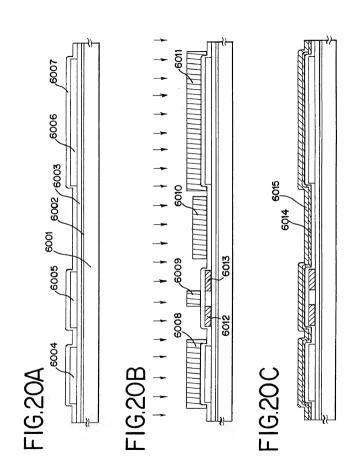
FIG.16C

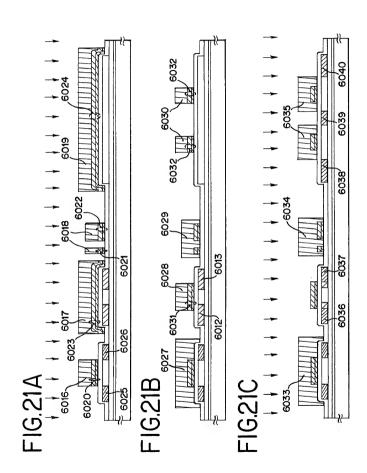


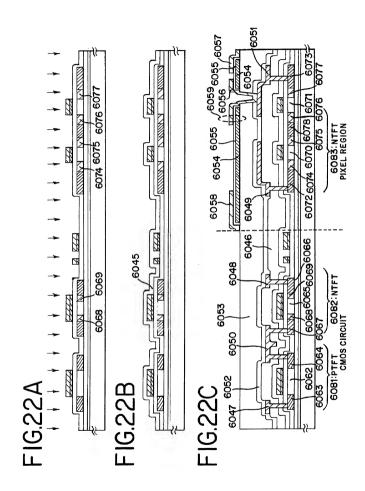












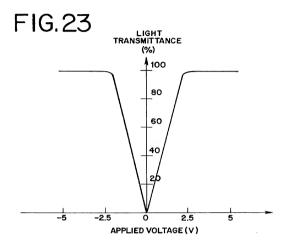


FIG. 24

2405

2404

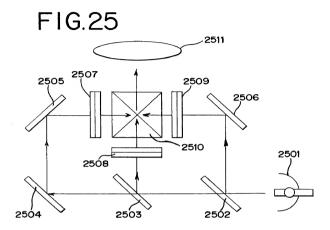
2408

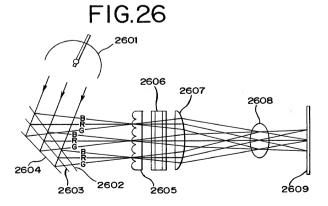
2406

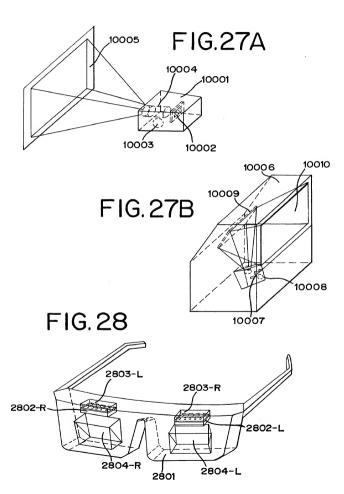
2407

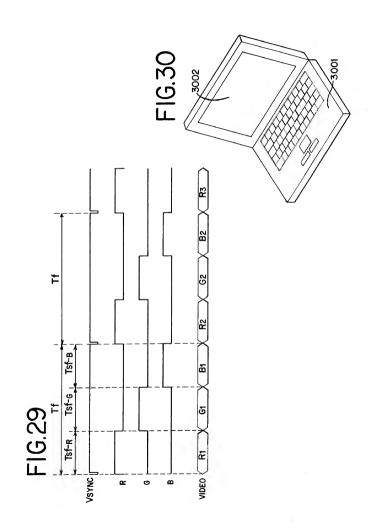
2403

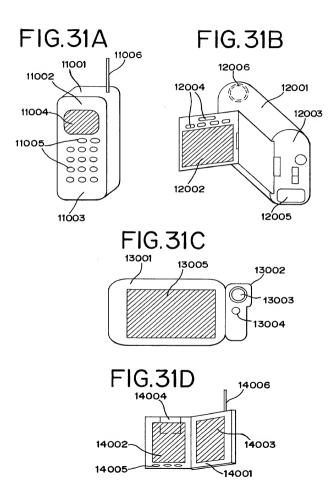
2402

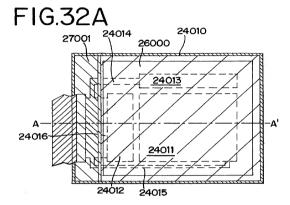












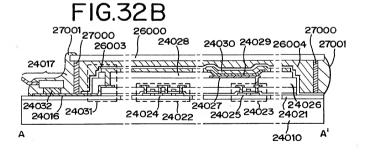


FIG.33A

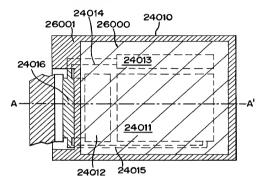
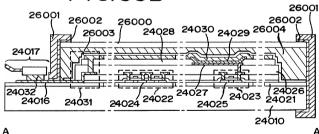


FIG.33B



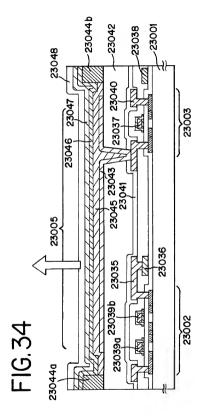


FIG.35A

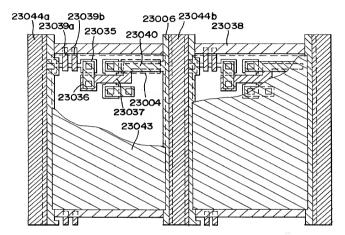
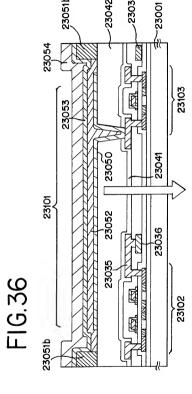


FIG. 35B
23002 23004

SOURCE SIGNAL LINE 23005
23006



.23038

23051b

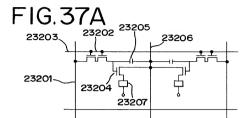


FIG.37B

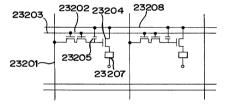


FIG.37C

